



Substitute for Form 1449 A & B/PTO		<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	10/765,931
		Confirmation Number	1863
		Filing Date	January 29, 2004
		First Named Inventor	Youichi KUKIMOTO
		Art Unit	2823
		Examiner Name	Khiem D. Nguyen
Sheet	1	of	1
		Attorney Docket Number	

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

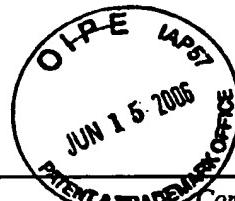
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K.M.		Copy of Office Action from Japanese Patent Office dated May 2, 2006.	

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MODIFIED PTO/SB/08 A & B (08-03)

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K.N.		Seiki SAKUYAMA, "Solder Bumping Technology for Wafer-scale Packaging", 7 th Symposium on "Microjoining and Assembly Technology in Electronics", February 1-2, 2001, pp 285-290.	Abstract

Examiner Signature *Karen M. Gandy* **Date Considered** *08/03/06*

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